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| FMEA NO. <u>2.1.2</u><br>CRITICALITY <u>2/2</u> | SHUTTLE CCTV<br>CRITICAL ITEMS LIST | UNIT <u>TVC/MLA</u><br>DWG NO. <u>2294819-506.508/</u><br><u>2294820-502</u><br>SHEET <u>1</u> OF <u>18</u> |
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| FAILURE MODE AND CAUSE   | FAILURE EFFECT ON END ITEM   | RATIONALE FOR ACCEPTANCE   |
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| <p>malfunction in the camera to cause total loss of the active video signal or video information that will provide degraded performance. Composite sync and vertical blanking signals are useable and not affected.</p> <p>Command Decode Logic.<br/>           Command Execution Logic.<br/>           Camera Timing Logic.<br/>           Horizontal Deflection.<br/>           Vertical Deflection.<br/>           Focus Current. Horizontal Alignment. Vertical Alignment.<br/>           Black/White Clipper.<br/>           Gamma Correction.<br/>           Aperture Correction.<br/>           Shading Correction.<br/>           Video Output Driver.<br/>           Automatic Light Control.<br/>           Automatic Gain Control.<br/>           Dark Current Compensation.<br/>           Output Voltage Regulator Input Voltage Pre-Regulator.<br/>           Primary Oscillator/Driver (sync loss)<br/>           Secondary Rectifiers/ Filters (change of voltage or loss of filtering)<br/>           High Voltage Power Supply.<br/>           Target Preamplifier.<br/>           Beam Current Regulator.<br/>           Cathode Blanking.<br/>           Shading Waveform Generator.<br/>           SIT Tube</p> | <p>(1) Unable to open lens iris.<br/>           (2) Degradation of picture content.</p> <p><u>Worst Case:</u><br/>           Loss of mission critical video.</p> | <p><b>DESIGN FEATURES</b></p> <p>The TVC/Lens Assembly is comprised of 16 electrical subassemblies; 13 subassemblies are RCA Astro designed and fabricated using standard printed-circuit board type of construction. The remaining three assemblies, high voltage power supply, oscillator, and stepper motors, are vendor supplied components which have been specified and purchased according to RCA Specification Control Drawings (SCDs) prepared by engineering and reliability assurance. Specifications per the SCD are prepared to establish the design, performance, test, qualification, and acceptance requirements for a procured piece of equipment.</p> <p>Parts, materials, processes, and design guidelines for the Shuttle CCTV program are specified in accordance with RCA 2295503. This document defines the program requirements for selection and control of EEE parts. To the maximum extent, and consistent with availability, all parts have been selected from military specifications at the JAN level, as a minimum. In addition to the overall selection criteria, a subset of general purpose preferred parts has been defined by this document and the RCA Government Systems Division Standard Parts List. In the case of the CMOS and TTL family of microcircuits, devices are screened and tested to the MIL-STD-883C equivalent and procured under the designations of HI-REL/3WQ and SNC 54LS from RCA-SSD and Texas Instruments Corp, respectively. Parts not included in the above documents have been used in the design only after a nonstandard item approval form (NSIAF) has been prepared, submitted to Reliability Assurance Engineering (RAE) and approved for use in the specific application(s) defined in the NSIAF by NASA-JSC.</p> <p>Worst-Case Circuit Analyses have been performed and documented for all circuit designs to demonstrate that sufficient operating margins exist for all operating conditions. The analysis was worst case-in that the value for each of the variable parameters was set to limits that will drive the output to a maximum (or minimum).</p> <p>A component application review and analysis was conducted to verify that the applied stress on each piece part by the temperature extremes identified with environmental qualification testing does not exceed the stress derating values identified in RCA 2295503.</p> <p>In addition, an objective examination of the design was performed through a PDR and CDR to verify that the TVC/Lens assembly met specification and contractual requirements.</p> |

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| FAILURE MODE AND CAUSE  | FAILURE EFFECT ON END ITEM   | RATIONALE FOR ACCEPTANCE  |
|---|--|---|
| <p>malfunction in the camera to cause total loss of the active video signal or video information that will provide degraded performance. Composite sync and vertical blanking signals are useable and not affected.</p> <p>Command Decode Logic.<br/>                     Command Execution Logic.<br/>                     Camera Timing Logic.<br/>                     Horizontal Deflection.<br/>                     Vertical Deflection.<br/>                     Focus Current. Horizontal Alignment.<br/>                     Vertical Alignment.<br/>                     Black/White Clipper.<br/>                     Gamma Correction.<br/>                     Aperture Correction.<br/>                     Shading Correction.<br/>                     Video Output Driver.<br/>                     Automatic Light Control.<br/>                     Automatic Gain Control.<br/>                     Dark Current Compensation.<br/>                     Output Voltage Regulator.<br/>                     Input Voltage Pre-Regulator.<br/>                     Primary Oscillator/Driver (sync loss)<br/>                     Secondary Rectifiers/ Filters (change of voltage or loss of filtering)<br/>                     High Voltage Power Supply.<br/>                     Target Preamplifier.<br/>                     Beam Current Regulator.<br/>                     Cathode Blanking.<br/>                     Shading Waveform Generator.<br/>                     SIT Tube</p> | <p>(1) Unable to open lens iris.<br/>                     (2) Degradation of picture content.</p> <p><u>Worst Case:</u><br/>                     Loss of mission critical video.</p> | <p><u>DESIGN FEATURES</u> (Continued)</p> <p><u>BARE BOARD CONSTRUCTION</u> (A2)</p> <p>The board is of "welded wire" construction. At the bare board level this does not distinguish it from a normal PC board except that holes which will take weld pins generally are not connected to PC traces. Only those pins which bring power and ground potentials to the ICs are on PCs. An annular ring surrounds the hole in the board where each power and ground pin is located. These pins are then soldered to the trace like any other component lead. Aside from this feature, all design &amp; construction techniques used in PC board layout apply.</p> <p><u>BOARD ASSEMBLY</u> (A2)</p> <p>The drilled and etched board is populated with several hundred solderable or weldable pins. Power and ground pins, as well as connector pins, are soldered in place. Discreet components (resistors, diodes, capacitors) are attached to bifurcated terminals, where they are soldered. Flatpack ICs are welded, lead-by-lead, to the tops of the weld pins. After welding, extra lead material is trimmed away. Circuit connections are made using #30 AWG nickel weld wire. The wire is welded to the pin surfaces on the board backside. All wire welds are done using a machine which is tape driven, thus eliminating the possibility of miswiring due to operator error. All wiring &amp; circuit performance is tested prior to box-level installation. After successful testing, components are staked as required by drawing notes and the assembly is coated with urethane.</p> <p>The board is inserted in the box on card-edge guides, in the same manner as the other PC boards.</p> |

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| FAILURE MODE AND CAUSE  | FAILURE EFFECT ON END ITEM   | RATIONALE FOR ACCEPTANCE  |
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| <p>malfunction in the camera to cause total loss of the active video signal or video information that will provide degraded performance. Composite sync and vertical blanking signals are useable and not affected.</p> <p>10 Command Decode Logic.<br/>                 11 Command Execution Logic.<br/>                 12 Camera Timing Logic.<br/>                 13 Horizontal Deflection.<br/>                 14 Vertical Deflection.<br/>                 15 Focus Current. Horizontal Alignment. Vertical Alignment.<br/>                 16 Black/White Clipper.<br/>                 17 Gamma Correction.<br/>                 18 Aperture Correction.<br/>                 19 Shading Correction.<br/>                 20 Video Output Driver.<br/>                 21 Automatic Light Control.<br/>                 22 Automatic Gain Control.<br/>                 23 Dark Current Compensation.<br/>                 24 Output Voltage Regulator.<br/>                 25 Input Primary Oscillator/Driver (sync loss)<br/>                 26 Secondary Rectifiers/ Filters (change of voltage or loss of filtering)<br/>                 27 High Voltage Power Supply.<br/>                 28 Target Preamplifier.<br/>                 29 Beam Current Regulator.<br/>                 30 Cathode Blanking.<br/>                 31 Shading Waveform Generator.<br/>                 32 S1T Tube</p> | <p>(1) Unable to open lens iris.<br/>                 (2) Degradation of picture content.</p> <p><u>Worst Case:</u><br/>                 Loss of mission critical video.</p> | <p><u>DESIGN FEATURES (Continued)</u></p> <p><b>BARE BOARD DESIGN (A3, A4, A5, A6, A7, A9, A10, A12)</b></p> <p>The (A3, A4, A5, A6, A7, A9, A10, and A12) boards are constructed from laminated copper-clad epoxy glass sheets (NEMA G-10) Grade FR-4), PER MIL-P-55617A. Circuit connections are made through printed traces which run from point to point on the board surfaces. Every trace terminates at an annular ring. The annular ring surrounds the hole in which a component lead or terminal is located. This ring provides a footing for the solder, ensuring good mechanical and electrical performance. Its size and shape are governed by MIL-P-55640 as are trace widths, spacing and routing. These requirements are reiterated specifically in drawing notes to further assure compliance. Variations between the artwork master and the final product (due to irregularities of the etching process) are also controlled by drawing notes. This prevents making defective boards from good artwork. Holes which house no lead or terminal, but serve only to electrically interconnect the different board layers, contain stitch bars for mechanical support and increased reliability.</p> <p>The thru holes are drilled from a drill tape thus eliminating the possibility of human error and allowing tight control over hole and annular ring concentricity, an important reliability criterion. After drilling and etching, All copper cladding is tin-lead plated per MIL-STD-1495. This provides for easy and reliable soldering at the time of board assembly, even after periods of prolonged storage.<br/>                 Voltage Pre-Regulator.</p> <p><b>BOARD ASSEMBLY DESIGN (A3, A4, A5, A6, A7, A9, A12)</b></p> <p>All components are installed in a manner which assures maximum reliability. Component leads are pre-tinned, allowing total wetting of solder joints. All leads are formed to provide stress relief and the bodies of large components are staked. Special mounting and handling instructions are included in each drawing required after final assembly. The board is coated with urethane which protects against humidity and contamination.</p> |

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|---|--|---|
| <p>malfunction in the camera to cause a loss of the active video signal or video information that will provide degraded performance. Composite sync and vertical blanking signals are useable and not affected.</p> <p>Command Decode Logic.<br/>Command Execution Logic.<br/>Camera Timing Logic.<br/>Horizontal Deflection.<br/>Vertical Deflection.<br/>Focus Current. Horizontal Alignment. Vertical Alignment.<br/>Black/White Clipper.<br/>Gamma Correction.<br/>Aperture Correction.<br/>Shading Correction.<br/>Video Output Driver.<br/>Automatic Light Control.<br/>Automatic Gain Control.<br/>Dark Current Compensation.<br/>Output Voltage Regulator.<br/>Input Voltage Pre-Regulator.<br/>Primary Oscillator/Driver (sync loss)<br/>Secondary Rectifiers/ Filters (change of voltage or loss of filtering)<br/>High Voltage Power Supply.<br/>Target Pre-amplifier.</p> <p>1 Beam Current Regulator.<br/>Cathode Blanking.<br/>2 Shading Waveform Generator.<br/>3 SIT tube</p> | <p>(1) Unable to open lens iris.<br/>(2) Degradation of picture content.</p> <p><u>Worst Case:</u><br/>Loss of mission critical video.</p> | <p><b>DESIGN FEATURES (Continued)</b></p> <p><b>BOARD MOUNTING (A10)</b></p> <p>Due to packaging constraints within the camera, the A10 TVC divider assembly is built in "cordwood" fashion. Two PC boards, each 2.1 inches square, are mounted opposite each other, separated by 3/4 inch long standoffs. Biaxial components span the distance between the boards and are soldered in place, one lead to each board. Some jumper wires are also mounted this way. All solder joints are staked. The boards have a 1-inch diameter hole through the center. The vidicon tube socket fits through this hole and has its flying leads soldered to thru-holes in one of the boards. At camera-level installation, the entire A10 module is inserted in place around the stem of the vidicon tube and bolted in four places to the housing. The socket is then engaged manually to the tube, completing the installation.</p> <p><b>BOARD MOUNTING (A9)</b></p> <p>The boards are mounted to the target preamp housing by 4 #4-40, screws. This provides ample support since the board is only 1.9 x 2.1 inches. Electrical correction is by jumper wires soldered to board terminals. A sheet metal cover is installed over the board to protect it during handling and installation of the target preamp assembly.</p> <p><b>BOARD PLACEMENT</b></p> <p>The A7-A low voltage power supply boards are bolted in place at 6 points around its perimeter. Four of these mounting screws also pass through and tie down the smaller A7-B board. These two boards are mounted face-to-face, separated by the standoffs. Electrical interconnections are achieved by jumper wires between the two boards. The A7-A houses a 34-pin connector which brings in power and signals from outside the module.</p> <p>The A7 module includes these two boards as well as power transistor Q4. The module housing is bent aluminum sheet, comprised of two halves screwed together. The boards and Q4 are secured to the lower half, and wired together. Then the upper half is put in place. By mounting Q4 directly to the aluminum housing, good thermal performance is assured.</p> <p>The A2, A3, A4, A5, A6, and A12 boards are secured in the electronics assembly by gold-plated beryllium copper card guides. Connections are made to the mother board with blind-mated connectors. Disengagement during launch is prevented by a cover which spans the board's free edge.</p> |

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| FAILURE MODE AND CAUSE  | FAILURE EFFECT ON END ITEM   | RATIONALE FOR ACCEPTANCE  |
|---|--|---|
| <p>malfunction in the camera to cause total loss of the active video signal or video information that will provide degraded performance. Composite sync and vertical blanking signals are useable and not affected.</p> <p>10 Command Decode Logic.<br/>           2 Command Execution Logic.<br/>           Camera Timing Logic.<br/>           Horizontal Deflection.<br/>           Vertical Deflection.<br/>           Vertical Alignment.<br/>           Black/White Clipper.<br/>           Gamma Correction.<br/>           Aperture Correction.<br/>           Shading Correction.<br/>           Video Output Driver.<br/>           2 Automatic Light Control.<br/>           Automatic Gain Control.<br/>           Dark Current Compensation.<br/>           2 Output Voltage Regulator.<br/>           Input Voltage Pre-Regulator.<br/>           7 Primary Oscillator/Driver (sync loss)<br/>           Secondary Rectifiers/ Filters (change of voltage or loss of filtering)<br/>           3 High Voltage Power Supply.<br/>           2 Target Preamplifier.<br/>           10 Beam Current Regulator.<br/>           Cathode Blanking.<br/>           12 Shading Waveform Generator.<br/>           14 SIT Tube</p> | <p>(1) Unable to open lens iris.<br/>           (2) Degradation of picture content.</p> <p><u>Worst Case:</u><br/>           Loss of mission critical video.</p> | <p>The high-voltage power supply is purchased to a performance specification that details the design, qualification, and production requirements for use in the TV Camera System. It is designed for a minimum operational life of 8000 hours and shall exhibit no evidence of arcing, corona, or any other intermittent or continuous failure when operated at any atmospheric pressure from sea level to <math>1 \times 10^{-6}</math> torr. All electronic components and materials used in the power supply design are verified to conform with the reliability requirements of the Shuttle CCTV program as outlined by NASA-JSC.</p> <p>The SIT tube is a selected high-grade silicon-diode-array target vidicon with an attached image intensifier for operating at low light levels. Faceplate exposure is controlled by means of an Automatic Light Control (ALC) function that restricts operation to <math>2 \times 10^{-2}</math> foot-candles. This can provide an SIT life of more than 1000 hours according to the manufacturer's recommendations. By providing exposure control when using the SIT tube, life can be extended to the point where reduced thermionic cathode emission will end its life, as in other camera tubes. Because of its excellent characteristics many military, medical, and scientific applications, in addition to surveillance applications, have been developed for low-light-level television cameras employing the SIT tube.</p> <p><u>QUALIFICATION TEST</u><br/>           For Qualification Test Flow, see Table 2 located at the front of this book.</p> |

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FAILURE MODE AND CAUSE

FAILURE EFFECT ON END ITEM

RATIONALE FOR ACCEPTANCE

malfunction in the camera to cause total loss of the active video signal or video information that will provide degraded performance. Composite sync and vertical blanking signals are useable and not affected.

- (1) Unable to open lens iris.
- (2) Degradation of picture content.

Worst Case:  
 Loss of mission critical video.

- 1 Command Decode Logic.
- 1 Command Execution Logic.
- 1 Camera Timing Logic.
- 1 Horizontal Deflection.
- 1 Vertical Deflection.
- 1 Vertical Alignment.
- 1 Black/White Clipper.
- 1 Gamma Correction.
- 1 Aperture Correction.
- 1 Shading Correction.
- 1 Video Output Driver.
- 2 Automatic Light Control.
- 2 Automatic Gain Control.
- 2 Dark Current Compensation.
- 2 Output Voltage Regulator.
- 1 Input Voltage Pre-Regulator.
- 1 Primary Oscillator/Driver (sync loss)
- 1 Secondary Rectifiers/ Filters (change of voltage or loss of filtering)
- 3 High Voltage Power Supply.
- 2 Target Preamplifier.
- 10 Beam Current Regulator.
- 1 Cathode Blanking.
- 12 Shading Waveform Generator.
- 14 SIT tube

ACCEPTANCE TEST

The CCTV systems' TVC/MLA is subjected directly, without vibration isolators which might be used in their normal installation, to the following testing:

- Vibration:
  - 20-80Hz: 3 dB/Oct-rise from 0.01 G<sup>2</sup>/Hz
  - 80-350 Hz: 0.04 G<sup>2</sup>/Hz
  - 350-750 Hz: -3 dB/10 Oct-slope
  - Test Duration: 1 Minute per Axis
  - Test Level: 6.1 Grms
- Thermal Vacuum: In a pressure of 1X10<sup>-5</sup> Torr, the temperature shall be as follows:
  - 125° F: Time to stabilize equipment plus 1 hour
  - 25° F: Time to stabilize equipment plus 1 hour
  - 125° F: Time to stabilize equipment plus 1 hour

The TVC/MLA may not have been subjected to the vacuum condition.

For Acceptance Test Flow, see Table 1 located at the front of this book.

OPERATIONAL TESTS

In order to verify that CCTV components are operational, a test must verify the health of all the command related components from the PHS (A7A1) panel switch, through the RCU, through the sync lines to the Camera/PTU, to the Camera/PTU command decoder. The test must also verify the camera's ability to produce video, the VSU's ability to route video, and the monitor's ability to display video. A similar test would be performed to verify the MDM command path.

Pre-Launch on Orbiter Test/In-Flight Test

1. Power CCTV System.
2. Via the PHS panel, select a monitor as destination and the camera under test as source.
3. Send "Camera Power On" command from PHS panel.
4. Select "External Sync" on monitor.
5. Observe video displayed on monitor. Note that if video on monitor is synchronized (i.e., stable raster) then this indicates that the camera is receiving composite sync from the RCU and that the camera is producing synchronized video.
6. Send Pan, Tilt, Focus, Zoom, DLR, AND Gamma commands and visually (either via the monitor or direct observation) verify operation.
7. Select downlink as destination and camera under test as source.
8. Observe video routed to downlink.
9. Send "Camera Power Off" command via PHS panel.
10. Repeat Steps 3 through 9 except issue commands via the MDM command path. This proves that the CCTV equipment is operational.

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| <p>malfunction in the camera to cause total loss of the active video signal or video information that will provide degraded performance. Composite sync and vertical blanking signals are useable and not affected.</p> <p>Command Decode Logic.<br/>Command Execution Logic.<br/>Camera Timing Logic.<br/>Horizontal Deflection.<br/>Vertical Deflection.<br/>Focus Current. Horizontal Alignment.<br/>Vertical Alignment.<br/>Black/White Clipper.<br/>Gamma Correction.<br/>Aperture Correction.<br/>Shading Correction.<br/>Video Output Driver.<br/>Automatic Light Control.<br/>Automatic Gain Control.<br/>Dark Current Compensation.<br/>Output Voltage Regulator.<br/>Input Voltage Pre-Regulator.<br/>Primary Oscillator/Driver (sync loss)<br/>Secondary Rectifiers/ Filters (change of voltage or loss of filtering)<br/>High Voltage Power Supply.<br/>Target Preamplifier.<br/>1 Beam Current Regulator.<br/>Cathode Blanking.<br/>2 Shading Waveform Generator.<br/>4 SIT Tube</p> | <p>(1) Unable to open lens iris.<br/>(2) Degradation of picture content.</p> <p><u>Worst Case:</u><br/>Loss of mission critical video.</p> | <p><u>QA/INSPECTION</u> (Continued)</p> <p style="text-align: center;"><u>IVC Assembly and Test</u></p> <p>An open box test is performed per TP-IT-2294819, and an Acceptance Test per TP-AT-2294819, including vibration and thermal vacuum. Torques are specified and witnessed, traceability numbers are recorded and calibrated tools are checked prior to use. RCA Quality and DCAS inspections are performed at the completion of specified FPR operations in accordance with PAI-204, PAI-205, PAI 206 and PAI 217. DCAS personnel witness TVC button-up and critical torquing.</p> <p><u>IVC/MLA Assembly and Test</u> - After a TVC and an MLA have been tested individually, they are mated and a final acceptance test is performed per TP-AT-2294819, including vibration and thermal vacuum environments. RCA and DCAS personnel monitor these tests and review the acceptance test data/results. These personnel also inspect for conformance after all repair, rework and retest.</p> <p><u>Preparation for Shipment</u> - The TVC and MLA are separated prior to shipment after fabrication and testing is complete. Each is packaged according to CCTV Letter 8011 and 2280746, Process standard for Packaging and Handling guidelines. All related documentation including assembly drawings, Parts List, ABPL, Test Data, etc., is gathered and held in a documentation folder assigned specifically to each assembly. This folder is retained for reference. An EIDP is prepared for each assembly in accordance with the requirements of WS-2593176. RCA QC and DCAS personnel witness crating, packaging, packing, and marking, and review the EIDP for completeness and accuracy.</p> |

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| FAILURE MODE AND CAUSE   | FAILURE EFFECT ON END ITEM   | RATIONALE FOR ACCEPTANCE   |
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| <p>malfunction in the camera to cause total loss of the active video signal or video information that will provide degraded performance. Composite sync and vertical blanking signals are useable and not affected.</p> <p>Command Decode Logic.<br/>Command Execution Logic.<br/>Camera Timing Logic.<br/>Horizontal Deflection.<br/>Vertical Deflection.<br/>Focus Current. Horizontal Alignment. Vertical Alignment.<br/>Black/White Clipper.<br/>Gamma Correction.<br/>Aperture Correction.<br/>Shading Correction.<br/>Video Output Driver.<br/>Automatic Light Control.<br/>Automatic Gain Control.<br/>Dark Current Compensation.<br/>Output Voltage Regulator Input Voltage Pre-Regulator.<br/>Primary Oscillator/Driver (sync loss)<br/>Secondary Rectifiers/ Filters (change of voltage or loss of filtering)<br/>High Voltage Power Supply.<br/>Target Preamplifier.<br/>Beam Current Regulator.<br/>Cathode Blanking.<br/>Shading Waveform Generator.<br/>SIT Tube</p> | <p>(1) Unable to open lens iris.<br/>(2) Degradation of picture content.</p> <p><u>Worst Case:</u><br/>Loss of mission critical video.</p> | <p><b>FAILURE HISTORY</b></p> <p>TDR - W2575 - Log #0400 - TVC S/N 005-501</p> <p><u>Description:</u> Acceptance Test Failure<br/>Box Level<br/>Vibration Environment<br/>Video presentation rotated approximately 6° after vibration.</p> <p><u>Cause:</u> Vidicon Yoke nut became loose during vibration.</p> <p><u>Corrective Action:</u> Reposition yoke, tighten nut and add staking to sensor assy's yoke nut. ECN (B1073) issued.</p> <p>TDR - W2576 -Log #0412 - TVC S/N 005-501</p> <p><u>Description:</u> Acceptance Test Failure<br/>Box Level<br/>Vibration Environment<br/>CW rotation of picture after "Y axis vibration. (Retest of TVC per TDR W2575.)</p> <p><u>Cause:</u> Bonding of yoke (A1) broke loose allowing yoke to rotate during vibration.</p> <p><u>Corrective Action:</u> Assembly procedure revised per ECN CCT 600 (B1073) to provide for better staking of yoke.</p> <p>TDR - W2560 - Log #0414 - TVC S/N 006-501</p> <p><u>Description:</u> Acceptance Test Failure<br/>Box Level<br/>Vibration Environment<br/>Input current increased by 180 mA from normal input current during vibration in "Y" Axis.</p> <p><u>Cause:</u> Defective capacitor C26 on A3 board. Analysis indicated manufacturing defect - capacitive element was loose inside metal case.</p> <p><u>Corrective Action:</u> Remove and replace part. Retested camera per PMO directive. Capacitors removed from stock and returned to vendor. Reference memo CCTV-(C)-243 for close out of TDR W2560.</p> |

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|------------------------|-------------------------------------|---|
| FMEA NO. <u>2.1.2</u>  | SHUTTLE CCTV<br>CRITICAL ITEMS LIST | UNIT <u>TVC/MLA</u>                                   |
| CRITICALITY <u>2/2</u> |                                     | DWG NO. <u>2294819-506.508/</u><br><u>2294820-502</u> |
|                        |                                     | SHEET <u>10</u> OF <u>18</u>                          |

| FAILURE MODE AND CAUSE   | FAILURE EFFECT ON END ITEM   | RATIONALE FOR ACCEPTANCE   |
|--|--|--|
| <p>malfunction in the camera to cause total loss of the active video signal or video information that will provide degraded performance. Composite sync and vertical blanking signals are useable and not affected.</p> <p>C<br/>                     Command Decode Logic.<br/>                     Command Execution Logic.<br/>                     Camera a Timing Logic.<br/>                     Horizontal Deflection.<br/>                     Vertical Deflection.<br/>                     Focus Current. Horizontal Alignment. Vertical Alignment.<br/>                     Black/White Clipper.<br/>                     Gamma Correction.<br/>                     Aperture Correction.<br/>                     Shading Correction.<br/>                     Video Output Driver.<br/>                     Automatic Light Control.<br/>                     Automatic Gain Control.<br/>                     Dark Current Compensation.<br/>                     Output Voltage Regulator Input Voltage Pre-Regulator.<br/>                     Primary Oscillator/Driver (sync loss)<br/>                     Secondary Rectifiers/ Filters (change of voltage or loss of filtering)<br/>                     B High Voltage Power Supply.<br/>                     9 Target Preamplifier.<br/>                     10 Beam Current Regulator.<br/>                     Cathode Blanking.<br/>                     12 Shading Waveform Generator.<br/>                     14 SIT Tube</p> | <p>(1) Unable to open lens iris.<br/>                     (2) Degradation of picture content.</p> <p><u>Worst Case:</u><br/>                     Loss of mission critical video.</p> | <p><b>FAILURE HISTORY (Continued)</b></p> <p>TDR - W0988 - Log #0432 - TVC S/N 005-501<br/>                     W0989 - Log #0428 - TVC S/N 005-501</p> <p><u>Description:</u> Acceptance Test Failure<br/>                     Box Level<br/>                     T/V Hot Environment<br/>                     Cluster of white spots in lower left quadrant of display on black field. Cracking of cement securing cover glass to fiberoptics faceplate evident.</p> <p><u>Cause:</u> Stresses exerted by potting material during temp. extremes resulted in de-lamination of the cover glass to SIT tube bonding.</p> <p><u>Corrective Action:</u> Modified potting fixtures and technique used on SIT tube Dwg. #2295530 modified by ECN (B 2082). SIT tubes returned to manufacturer for rework of encapsulation. Vendor to perform temp tests on tube prior to shipment to insure cover glass does not shift. REF CCTV Directives CCTV - (D) - 054, CCTV - (D) - 070.</p> <p>TDR - W2739 - Log #0485 TVC S/N 009-502</p> <p><u>Description:</u> Pre-Launch Failure<br/>                     Box Level Test<br/>                     Ambient Environment<br/>                     Problem report VSCS-201-0093 visual inspection of unit at KSC revealed a recessed pin 31 in the J1 connector.<br/>                     (Location code function pin)</p> <p><u>Cause:</u> Error in manufacturing of the connector pin. It is 0.035" shorter in length than other pins in connector.</p> <p><u>Corrective Action:</u> Remove and replace pin 31 in J1 connector. Test pins ability to lock into mating connector. Perform contact retention test to insure pin is locking as required. Inspect to insure pin is not pushed out of place.</p> <p>TDR - W0401 - Log #0499 - TVC 013-502<br/>                     TDR - W4278 - Log #0497 - TVC 013-502</p> <p><u>Description:</u> Qualification Test Failure<br/>                     Box Level (Qual. Model)<br/>                     Ambient Environment<br/>                     Output video intermittent.</p> <p><u>Cause:</u> SIT has low G4 current.</p> |

FMEA NO. 2.1.2  
 CRITICALITY 2/2

SHUTTLE CCTV  
 CRITICAL ITEMS LIST

UNIT TVC/MLA  
 DWG NO. 2294819-506.508/  
2294820-502  
 SHEET 11 OF 18

FAILURE MODE AND  
 CAUSE

malfunction in the camera to cause  
 al loss of the active video sig-  
 or video information that will  
 vide degraded performance. Com-  
 ite sync and vertical blanking  
 nals are useable and not affected.

- Command Decode Logic.
- Command Execution Logic.
- Camera a Timing Logic.
- Horizontal Deflection.
- Vertical Deflection.
- Focus Current. Horizontal Align-  
 ment. Vertical Alignment.
- Black/White Clipper.
- Gamma Correction.
- Aperture Correction.
- Shading Correction.
- Video Output Driver.
- Automatic Light Control.
- Automatic Gain Control.
- Dark Current Compensation.
- Output Voltage Regulator Input
- Voltage Pre-Regulator.
- Primary Oscillator/  
 Driver (sync loss)
- Secondary Rectifiers/ Filters  
 (change of voltage or loss of  
 filtering)
- High Voltage Power Supply.
- Target Pre-amplifier.
- Beam Current Regulator.
- Cathode Blanking.
- Shading Waveform Generator.
- SIT Tube

FAILURE EFFECT  
 ON END ITEM

- (1) Unable to open lens  
 iris.
  - (2) Degradation of pic-  
 ture content.
- Worst Case:  
 Loss of mission critical  
 video.

RATIONALE FOR ACCEPTANCE

FAILURE HISTORY (Continued)

Corrective Action: Remove and replace SIT. Defective tube returned to Lancaster for evaluation, Results: Tube failure verified.

TDR - W4312 - Log #0506 - TVC S/N 014-502

Description: Acceptance Test Failure  
 Box Level

Thermal Vac-Hot Environment

Approx. 45 minutes into (T/V Hot) soak at +125°F video became distorted and TVC input current increased 710 mA to 800 mA.

Cause: Yoke deflection coil developed a short between horizontal and vertical deflection windings at 50°C. The short was eliminated when the temperature was reduced.

Corrective Action: Replaced defective yoke with new part. This is a random failure.

TDR - W4677 - Log #0529 - TVC S/N 009-502

Description: Integration Test Failure  
 Box Level

Ambient Environment

Center resolution measured 30% @ 300 TVL, should be 70% min. Amplitude appears appears to be intermittent.

Cause: Transformer T3 on the A7 Board had unusual interwinding capacity, causing ringing on the secondary resulting in a slightly higher output voltage.

Corrective Action: Interwinding capacity is not a parameter specified, nor can it be measured readily. Transmitter T3 removed & replaced. Acceptance Test screening will detect this problem if present on other units - This problem is considered a random failure.

TDR - Y1781 - Log #0577, TVC S/N 017-504

Description: Acceptance Test Failure  
 Box Level

Ambient Environment

Oscillations in video output.

Cause: Poor grounding in pre-amp.

FMEA NO. 2.1.2  
 CRITICALITY 2/2

SHUTTLE CCTV  
 CRITICAL ITEMS LIST

UNIT TVC/MLA  
 DWG NO. 2294819-506,508/  
2294820-502  
 SHEET 12 OF 18

FAILURE MODE AND  
 CAUSE

FAILURE EFFECT  
 ON END ITEM

RATIONALE FOR ACCEPTANCE

malfunction in the camera to cause total loss of the active video signal or video information that will provide degraded performance. Composite sync and vertical blanking signals are useable and not affected.

(1) Unable to open lens iris.  
 (2) Degradation of picture content.  
  
**Worst Case:**  
 Loss of mission critical video.

FAILURE HISTORY (Continued)

- Command Decode Logic.
- Command Execution Logic.
- Camera Timing Logic.
- Horizontal Deflection.
- Vertical Deflection.
- Focus Current. Horizontal Alignment. Vertical Alignment.
- Black/White Clipper.
- Gamma Correction.
- Aperture Correction.
- Shading Correction.
- Video Output Driver.
- Automatic Light Control.
- Automatic Gain Control.
- Dark Current Compensation.
- Output Voltage Regulator Input Voltage Pre-Regulator.
- Primary Oscillator/Driver (sync loss)
- Secondary Rectifiers/ Filters (change of voltage or loss of filtering)
- High Voltage Power Supply.
- Target Preamplifier.
- Beam Current Regulator.
- Cathode Blanking.
- Shading Waveform Generator.
- SIT Tube

**Corrective Action:** Unit reworked to ECN CCT 830 for ground problem.

TDR - W1729 - Log #0578 - TVC S/N 020-502  
 TDR - W1730 - Log #0579 - WLA S/N 006-501  
 TDR - Y1404 - Log #0565 - TVC S/N 011-502

**Description:** Flight Failure Spacecraft Level  
 TVC S/N 020, WLA S/N 006-STS-3  
 TVC S/N 011 - STS-2  
 Coherent noise in output video presentation.

**Cause:** Poor conductive path to ground caused by excessive conformal coating and oxidation of aluminum spacers.

**Corrective Action:** Removed excessive conformal coating. Spacers were cleaned and treated with alodine. Flow progress report has been modified to insure proper assembly of units.

TDR - Y6906 - Log #0630 - TVC S/N 019-504

**Description:** Flight Failure  
 STS-5  
 Problem report VJCS-024  
 Intermittent Pan/Tilt reset command.

**Cause:** Problem could not be verified thru extensive thermal testing.

**Corrective Action:** Unit updated from group 504 to group 506 configuration. Instructed test director to carefully monitor operation of this camera during retesting. failure still could not be verified.

TDR - W6859 - Log #0695 - TVC S/N 024-506

**Description:** Pre-Launch Test Failure  
 Box Level  
 Ambient Environment  
 Automatic Iris goes from open to close.

**Cause:** Problem could not be duplicated after extensive testing.

**Corrective Action:** None - unit returned to KSC.

| MEA NO. <u>2.1.2</u><br>CRITICALITY <u>2/2</u>  | SHUTTLE CCTV<br>CRITICAL ITEMS LIST  | UNIT <u>TVC/MLA</u><br>DWG NO. <u>2294819-506, 508/<br/>2294820-502</u><br>SHEET <u>13</u> OF <u>18</u>   |
|---|--|---|
| FAILURE MODE AND CAUSE  | FAILURE EFFECT ON END ITEM   | RATIONALE FOR ACCEPTANCE  |
| <p>                             malfunction in the camera to cause<br/>                             loss of the active video sig-<br/>                             or video information that will<br/>                             degrade performance. Com-<br/>                             ite sync and vertical blanking<br/>                             als are useable and not affected.                         </p> <p>                             Command Decode Logic.<br/>                             Command Execution Logic.<br/>                             Camera a Timing Logic.<br/>                             Horizontal Deflection.<br/>                             Vertical Deflection.<br/>                             Focus Current. Horizontal Align-<br/>                             ment. Vertical Alignment.<br/>                             Black/White Clipper.<br/>                             Gamma Correction.<br/>                             Aperture Correction.<br/>                             Shading Correction.<br/>                             Video Output Driver.<br/>                             Automatic Light Control.<br/>                             Automatic Gain Control.<br/>                             Dark Current Compensation.<br/>                             Output Voltage Regulator Input<br/>                             Voltage Pre-Regulator.<br/>                             Primary Oscillator/<br/>                             Driver (sync loss)<br/>                             Secondary Rectifiers/ Filters<br/>                             (change of voltage or loss of<br/>                             filtering)<br/>                             High Voltage Power Supply.<br/>                             Target Preamplifier.<br/>                             Beam Current Regulator.<br/>                             Cathode Blanking.<br/>                             Shading Waveform Generator.<br/>                             SIT Tube                         </p> | <p>                             (1) Unable to open lens<br/>                             iris.<br/>                             (2) Degradation of pic-<br/>                             ture content.                         </p> <p>                             Worst Case:<br/>                             Loss of mission critical<br/>                             video.                         </p> | <p>                             FAILURE HISTORY (Continued)                         </p> <p>                             TDR - W1735 - Log #0723 - TVC S/N N/A -506                         </p> <p>                             Description: Acceptance Test Failure<br/>                             Box level<br/>                             Ambient Environment<br/>                             High voltage power supply S/N 2046 exhibits internal arcing. This TDR was<br/>                             opened to investigate failure noted on TDR-W6870, Log #722.                         </p> <p>                             Cause: Improper lead dress of high voltage terminal.                         </p> <p>                             Corrective Action: HVPS S/N 2046 was one of eight units manufactured under<br/>                             purchase order G-T50999-4151-00F28 by Murata Erie. This problem is limited to<br/>                             units manufactured in 1982. All defective units have been screened and returned<br/>                             to Murata Erie for analysis and replacement.                         </p> <p>                             TDR - W6870 - Log #0722 - TVC S/N 02B-506                         </p> <p>                             Description: Acceptance Test Failure<br/>                             Ambient Environment<br/>                             Box Level Test<br/>                             Approximately 47 seconds after initial turn on, the iris close command would be<br/>                             generated by TVC.                         </p> <p>                             Cause: Internal short in the high voltage power supply (S/N 2046), which<br/>                             generated spikes on the POR Line.                         </p> <p>                             Corrective Action: H.V.P.S. removed from TVC and new H.V.P.S. installed.<br/>                             Tests indicated problem solved. Power supply returned to vendor for Evaluation.<br/>                             Refer to TDR W1735, Log #723.<br/>                             The problem was attributed to improper lead dress of high voltage terminal.<br/>                             These leads have been rerouted and extra insulation added at the HV terminal<br/>                             for all new built units.                         </p> <p>                             TDR - W-6875 - Log #0746 TVC S/N 014-506<br/>                             TDR - W-6872 - Log #0742 TVC S/N 011-506                         </p> <p>                             Cause: Resistor R6 on A10 board, failed.                         </p> <p>                             Corrective Action: All flight cameras containing R6 resistor fabricated by ACI<br/>                             and dated with code 7813 will be replaced with new RNC60 type resistors from TRW.                         </p> |

| FMEA NO. <u>2.1.2</u><br>CRITICALITY <u>2/2</u>  | SHUTTLE CCTV<br>CRITICAL ITEMS LIST  | UNIT <u>TVC/MLA</u><br>DWG NO. <u>2294819-506.508/</u><br><u>2294820-502</u><br>SHEET <u>14</u> OF <u>18</u>  |
|--|--|---|
| FAILURE MODE AND CAUSE   | FAILURE EFFECT ON END ITEM   | RATIONALE FOR ACCEPTANCE  |
| <p>malfunction in the camera to cause total loss of the active video signal or video information that will provide degraded performance. Composite sync and vertical blanking signals are useable and not affected.</p> <p><u>YC</u><br/>2 Command Decode Logic.<br/>Command Execution Logic.<br/>Camera Timing Logic.<br/>3 Horizontal Deflection.<br/>Vertical Deflection.<br/>Focus Current. Horizontal Alignment. Vertical Alignment.<br/>4 Black/White Clipper.<br/>Gamma Correction.<br/>Aperture Correction.<br/>Shading Correction.<br/>Video Output Driver.<br/>5 Automatic Light Control.<br/>Automatic Gain Control.<br/>Dark Current Compensation.<br/>6 Output Voltage Regulator Input<br/>Voltage Pre-Regulator.<br/>7 Primary Oscillator/<br/>Driver (sync loss)<br/>Secondary Rectifiers/ Filters<br/>(change of voltage or loss of filtering)<br/>8 High Voltage Power Supply.<br/>9 Target Preamplifier.<br/>10 Beam Current Regulator.<br/>Cathode Blanking.<br/>12 Shading Waveform Generator.<br/>14 SIT Tube</p> | <p>(1) Unable to open lens iris.<br/>(2) Degradation of picture content.</p> <p><u>Worst Case:</u><br/>Loss of mission critical video.</p> | <p><u>FAILURE HISTORY (Continued)</u></p> <p>TDR - W6917 - Log #0748 - TVC S/N 030-506</p> <p><u>Description:</u> Acceptance Test Failure<br/>Box Level<br/>Ambient Environment<br/>Video presentation very noisy when J4 cable is connected.</p> <p><u>Cause:</u> Sensor assembly clamp sleeve shorted to chassis. Insufficient insulation coating on clamp sleeve.</p> <p><u>Corrective Action:</u> New clamps were installed. All parts in controlled stores were inspected and all discrepant material rejected.</p> <p>TDR - W6925 - Log #0770 - TVC S/N 022-506</p> <p><u>Description:</u> Flight Failure (STS-B)<br/>Spacecraft Level</p> <p>Horizontal streaking in picture gamma when in black stretch.</p> <p><u>Cause:</u> Incorrect shim installed. Face plate shim shorted to ground.</p> <p><u>Corrective Action:</u> Shim removed and correct shim installed per ECN-CCT859. All future TVC's will conform to this ECN.</p> <p>TDR - A3249 - Log #0929 - TVC S/N 033-506</p> <p><u>Description:</u> Acceptance Test Failure<br/>Box Level<br/>Thermal Vac. Ambient Environment<br/>Video locked in white clip mode.</p> <p><u>Cause:</u> Defective Transistor Q16 on A5 Board.</p> <p><u>Corrective Action:</u> Removed Q16 and replaced with new transistor. Q16 X-rayed and a dense particle was observed. Further analysis proved to be a loose and metallic particle. Considered to be random failure.</p> <p>TDR - A4093 - Log #0973 TVC S/N 019-506</p> <p><u>Description:</u> Acceptance Test Failure<br/>Box Level<br/>Vibration Environment<br/>Unit failed "Y" axis vibration, picture on monitor all white.</p> |

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| MEA NO. <u>2.1.2</u>   | SHUTTLE CCTV<br>CRITICAL ITEMS LIST | UNIT <u>TVC/MLA</u>             |
| CRITICALITY <u>2/2</u> |                                     | DWG NO. <u>2294819-506.508/</u> |
|                        |                                     | <u>2294820-502</u>              |
|                        |                                     | SHEET <u>15</u> OF <u>18</u>    |

| FAILURE MODE AND CAUSE  | FAILURE EFFECT ON END ITEM   | RATIONALE FOR ACCEPTANCE  |
|---|--|---|
| <p>malfunction in the camera to cause a loss of the active video signal or video information that will degrade performance. Composite sync and vertical blanking signals are useable and not affected.</p>  | <p>(1) Unable to open lens iris.<br/>(2) Degradation of picture content.</p> <p><u>Worst Case:</u><br/>Loss of mission critical video.</p> | <p><b>FAILURE HISTORY (Continued)</b></p> <p><u>Cause:</u> Defective Q7 on the A4 board.</p> <p><u>Corrective Action:</u> Product Assurance analysis revealed an open transistor Q7. They found an extraneous gold bond wire positioned so as to cause a short between the base leads of the dual transistors. Transistors with the same date code, plus all transistors with different data codes were X-rayed and no anomalies were found. Concluded that this was a random failure.</p> <p>TDR - W1746 - Log #1024, TVC S/N 011-506</p> <p><u>Description:</u> Pre-Launch Test Failure<br/>Box Level<br/>Ambient Environment<br/>KSC DR.3C430012</p> <p>ALC and Gamma controls intermittent. Pan and tilt operation intermittent.</p> <p><u>Cause:</u> Intermittent operation of ALC and Gamma commands could not be duplicated thru extensive thermal testing. Intermittent tilt motion due to improper position of right hand camera slide.</p> <p><u>Corrective Action:</u> Slides were re-positioned per ECN-CCT 1178. Retest indicated problem solved.<br/>Returned TVC 011 to "JSC" for further evaluation of ALC/GAMMA anomaly.</p> <p>TDR - B0146 - Log #1079 - TVC S/N 007-506</p> <p><u>Description:</u> Flight Failure<br/>Flight #410<br/>Evaluation Test</p> <p>TVC returned from KSC for evaluation of white streaks in video observed during orbiter flight #410.</p> <p><u>Cause:</u> Arcing in vicinity of ground tab of face plate of SIT tube S/N J8294.</p> <p><u>Corrective Action:</u> Removed tube S/N J8249 from sensor assembly, opened TDR-B0809, Log #1105 to track tube; tube returned to vendor.<br/>Installed new tube into sensor.</p> <p>TDR - B-0148 - Log #1085 - TVC S/N 044-506</p> <p><u>Description:</u> Acceptance Test Failure<br/>Box Level<br/>Thermal-Vac Ambient Environment</p> <p>Distorted video seen on monitor. Distortion consisted of two types of noise, modulated and random.</p> |
| <p>Command Decode Logic.<br/>Command Execution Logic.<br/>Camera Timing Logic.<br/>Horizontal Deflection.<br/>Vertical Deflection.<br/>Focus Current. Horizontal Alignment. Vertical Alignment.<br/>Black/White Clipper.<br/>Gamma Correction.<br/>Aperture Correction.<br/>Shading Correction.<br/>Video Output Driver.<br/>Automatic Light Control.<br/>Automatic Gain Control.<br/>Dark Current Compensation.<br/>Output Voltage Regulator Input<br/>Voltage Pre-Regulator.<br/>Primary Oscillator/<br/>Driver (sync loss)<br/>Secondary Rectifiers/ Filters (change of voltage or loss of filtering)<br/>High Voltage Power Supply.<br/>Target Preamplifier.<br/>Beam Current Regulator.<br/>Cathode Blanking.<br/>Shading Waveform Generator.<br/>SIT Tube</p> |  |   |

| FMEA NO. <u>2.1.2</u><br>CRITICALITY <u>2/2</u>  |  | SHUTTLE CCTV<br>CRITICAL ITEMS LIST  | UNIT <u>TVC/MLA</u><br>DWG NO. <u>2294819-506.508/</u><br><u>2294820-502</u><br>SHEET <u>16</u> OF <u>18</u> |
|--|--|--|--|
| FAILURE MODE AND CAUSE   | FAILURE EFFECT ON END ITEM   | RATIONALE FOR ACCEPTANCE   |  |
| <p>malfunction in the camera to cause total loss of the active video signal or video information that will provide degraded performance. Composite sync and vertical blanking signals are useable and not affected.</p> <p>1 Command Decode Logic.<br/>                     2 Command Execution Logic.<br/>                     Camera Timing Logic.<br/>                     3 Horizontal Deflection.<br/>                     Vertical Deflection.<br/>                     Focus Current. Horizontal Alignment.<br/>                     Vertical Alignment.<br/>                     4 Black/White Clipper.<br/>                     Gamma Correction.<br/>                     Aperture Correction.<br/>                     Shading Correction.<br/>                     Video Output Driver.<br/>                     5 Automatic Light Control.<br/>                     Automatic Gain Control.<br/>                     Dark Current Compensation.<br/>                     6 Output Voltage Regulator Input Voltage Pre-Regulator.<br/>                     7 Primary Oscillator/Driver (sync loss)<br/>                     Secondary Rectifiers/ Filters (change of voltage or loss of filtering)<br/>                     8 High Voltage Power Supply.<br/>                     9 Target Preamplifier.<br/>                     10 Beam Current Regulator.<br/>                     Cathode Blanking.<br/>                     12 Shading Waveform Generator.<br/>                     14 SIT Tube</p> | <p>(1) Unable to open lens iris.<br/>                     (2) Degradation of picture content.</p> <p><u>Worst Case:</u><br/>                     Loss of mission critical video.</p> | <p><u>FAILURE HISTORY</u> (Continued)</p> <p><u>Cause:</u> Random noise is caused by poor grounding of the A9 pre-amp. Modulated noise is due to A5 board operation.</p> <p><u>Corrective Action:</u> Random noise problem was cured by cleaning the A9 ground connections and the incorporation of ECN D09485 - redundant wire ground connection to the A9 pre-amp. Modulated noise problem was cured by incorporating ECN E1773 to the A5 board which limits the control voltage to the AB high voltage power supply. TVC Group number was changed from 2294819-506 to 2294819-508.</p> <p>TDR - B-2352 - Log # 1123 TVC S/N 033-506</p> <p><u>Description:</u> Pre-Launch Test Failure<br/>                     Box Level<br/>                     Ambient Environment<br/>                     (Ref) 3CA420062) Noisy video, vertical lines with low light level.</p> <p><u>Cause:</u> Poor A9 pre-amplifier ground.</p> <p><u>Corrective Action:</u> ECN CCT 1237. Written to add redundant ground lead.</p> <p>TDR - B3451 - Log #1150 S/N 042-508</p> <p><u>Description:</u> Acceptance Test Failure<br/>                     Box Level<br/>                     Vibration Environment<br/>                     Vertical noise pattern present in video.</p> <p><u>Cause:</u> Poor ground connection on A9 pre-amplifier board thru conductive washers.</p> <p><u>Corrective Action:</u> ECN D-9584 (Ground wire installation) was installed in sensor assembly. This ECN incorporated in all previous units presently located at RCA, and all new build units.</p> <p>TDR - B3518 - Log #1174 - TVC S/N 020-506</p> <p><u>Description:</u> Pre-Launch Test Failure<br/>                     Box Level<br/>                     Ambient Environment<br/>                     Problem report #DR.3C53001A<br/>                     Evaluate video noise problem</p> <p><u>Cause:</u> Poor grounding for pre-amp. TVC depended on a mechanical ground connection.</p> |  |

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|--|-------------------------------------|--|
| DRAWING NO. <u>2.1.2</u><br>ITERATION <u>2/2</u> | SHUTTLE CCTV<br>CRITICAL ITEMS LIST | UNIT <u>TVC/MLA</u><br>DWG NO. <u>2294819-506.508/</u><br><u>2294820-502</u><br>SHEET <u>17</u> OF <u>18</u> |
|--|-------------------------------------|--|

| FAILURE MODE AND CAUSE   | FAILURE EFFECT ON END ITEM   | RATIONALE FOR ACCEPTANCE   |
|--|--|--|
| <p>function in the camera to cause loss of the active video signal or video information that will degrade performance. Composite sync and vertical blanking pulses are useable and not affected.</p> <p>Command Decode Logic.<br/>           Command Execution Logic.<br/>           Camera Timing Logic.<br/>           Horizontal Deflection.<br/>           Vertical Deflection.<br/>           Focus Current. Horizontal Alignment.<br/>           Vertical Alignment.<br/>           Black/White Clipper.<br/>           Gamma Correction.<br/>           Contrast Correction.<br/>           Shading Correction.<br/>           Video Output Driver.<br/>           Automatic Light Control.<br/>           Automatic Gain Control.<br/>           Dark Current Compensation.<br/>           Output Voltage Regulator Input Voltage Pre-Regulator.<br/>           Primary Oscillator/Driver (sync loss)<br/>           Secondary Rectifiers/ Filters (change of voltage or loss of filtering)<br/>           High Voltage Power Supply.<br/>           Target Preamplifier.<br/>           Beam Current Regulator.<br/>           Cathode Blanking.<br/>           Shading Waveform Generator.<br/>           SIT Tube</p> | <p>(1) Unable to open lens iris.<br/>           (2) Degradation of picture content.</p> <p><u>Worst Case:</u><br/>           Loss of mission critical video.</p> | <p><u>FAILURE HISTORY (Continued)</u></p> <p><u>Corrective Action:</u> ECN CCT-1237 was prepared adding redundant hard wire ground. This ECN was incorporated into TVC S/N 020 and retest indicated problem resolved. This ECN will be incorporated into all TVC's in process.</p> <p>TDR - C0751 - Log #2002 - TVC S/N 024-508<br/>           TDR - A3105 - Log #2097 - TVC S/N 024-508</p> <p><u>Description:</u> Acceptance Test Failure<br/>           Box Level<br/>           Ambient Environment<br/>           White ion spot visible in center of raster. Tube conditioning procedure operated in excess of 60 Hrs. which, reduced spot size to 20-25 IRE. Unit authorized to ship to NASA/JSC for further evaluation. The above unit returned from NASA (Ref. PR-DR 3C530032) with the 20-25 IRE spot.</p> <p><u>Cause:</u> TVC tube operated in an unauthorized configuration or mode such as (1) extended exposure to bright light (2) loss of camera voltages, (3) exposed to light without lens assembly.</p> <p><u>Corrective Action:</u> SIT replaced with new tube. Old tube sent to RCA-Lancaster for analysis. Found silicon target burned. No corrective action possible on tube S/N 243886. Tube to be labeled as non-flight, but may be used in non-critical application such as for TVC trainer.</p> <p>TDR - C4523 - Log #2039 - TVC 044-508</p> <p><u>Description:</u> Evaluation Test<br/>           Box Level<br/>           Ambient Environment<br/>           Output of high voltage power supply 0V with 6.0 Vdc control voltage, should be approximately 9 kv intermittent operation, slight rap on housing will produce 9 kv.</p> <p><u>Cause:</u> Analysis of failure under investigation.</p> <p><u>Corrective Action:</u> N/A</p> <p>TDR - C0693 - Log #2042 - TVC S/N 042-508</p> <p><u>Description:</u> Flight Failure STS 51F (26). Spacecraft Level<br/>           String like material on lens or TVC noticed in video presentation, also horizontal noise is present in output video.</p> |

MEA NO. 2.1.2  
 CRITICALITY 2/2

SHUTTLE CCTV  
 CRITICAL ITEMS LIST

UNIT TVC/MLA  
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| FAILURE MODE AND CAUSE  | FAILURE EFFECT ON END ITEM   | RATIONALE FOR ACCEPTANCE  |
|---|--|---|
| <p>malfunction in the camera to cause a) loss of the active video signal or video information that will degrade performance. Composite sync and vertical blanking signals are useable and not affected.</p> <p>Command Decode Logic.<br/>                     Command Execution Logic.<br/>                     Camera Timing Logic.<br/>                     Horizontal Deflection.<br/>                     Vertical Deflection.<br/>                     Focus Current. Horizontal Alignment.<br/>                     Vertical Alignment.<br/>                     Black/White Clipper.<br/>                     Gamma Correction.<br/>                     Aperture Correction.<br/>                     Shading Correction.<br/>                     Video Output Driver.<br/>                     Automatic Light Control.<br/>                     Automatic Gain Control.<br/>                     Dark Current Compensation.<br/>                     Output Voltage Regulator Input Voltage Pre-Regulator.<br/>                     Primary Oscillator/Driver (sync loss)<br/>                     Secondary Rectifiers/ Filters (change of voltage or loss of filtering)<br/>                     High Voltage Power Supply.<br/>                     Target Preamplifier.<br/>                     Beam Current Regulator.<br/>                     Cathode Blanking.<br/>                     Shading Waveform Generator.<br/>                     SIT Tube</p> | <p>(1) Unable to open lens iris.<br/>                     (2) Degradation of picture content.</p> <p><u>Worst Case:</u><br/>                     Loss of mission critical video.</p> | <p><u>FAILURE HISTORY</u> (Continued)</p> <p><u>Cause:</u> String like material was found on face of SIT tube and could not be determined where it came from. Horizontal noise could not be duplicated.</p> <p><u>Corrective Action:</u> String removed with small brush. This is the first reported instance of such contamination. Horizontal noise in video as reported could not be duplicated during 9 days of thermal cycling and thermal vacuum testing. The probable cause of the video noise was external to the TVC and unknown at this time.</p> |

FMEA NO. 2.1.2

CRITICALITY 2/2

SHUTTLE CCTV  
CRITICAL ITEMS LIST

UNIT TVC/MLA  
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FAILURE MODE AND  
CAUSE

FAILURE EFFECT  
ON END ITEM

RATIONALE FOR ACCEPTANCE

malfunction in the camera to cause total loss of the active video signal or video information that will provide degraded performance. Composite sync and vertical blanking signals are useable and not affected.

- (1) Unable to open lens iris.
  - (2) Degradation of picture content.
- Worst Case:  
Loss of mission critical video.

OPERATIONAL EFFECTS

Loss of video. Possible loss of major mission objectives due to loss of RMS cameras or other required cameras.

CREW ACTIONS

If possible, continue RMS operations using alternative visual cues.

CREW TRAINING

Crew should be trained to use possible alternatives to CCTV.

MISSION CONSTRAINT

Where possible, procedures should be designed so they can be accomplished without CCTV.

- 1 Command Decode Logic.
- 1 Command Execution Logic.
- 1 Camera Timing Logic.
- 1 Horizontal Deflection.
- 1 Vertical Deflection.
- 1 Focus Current. Horizontal Alignment.
- 1 Vertical Alignment.
- 1 Black/White Clipper.
- 1 Gamma Correction.
- 1 Aperture Correction.
- 1 Shading Correction.
- 1 Video Output Driver.
- 1 Automatic Light Control.
- 1 Automatic Gain Control.
- 1 Dark Current Compensation.
- 1 Output Voltage Regulator.
- 1 Input Voltage Pre-Regulator.
- 1 Primary Oscillator/Driver (sync loss)
- 1 Secondary Rectifiers/ Filters (change of voltage or loss of filtering)
- 1 High Voltage Power Supply.
- 1 Target Preamplifier.
- 0 Beam Current Regulator.
- 2 Cathode Blanking.
- 2 Shading Waveform Generator.
- 4 SIT Tube